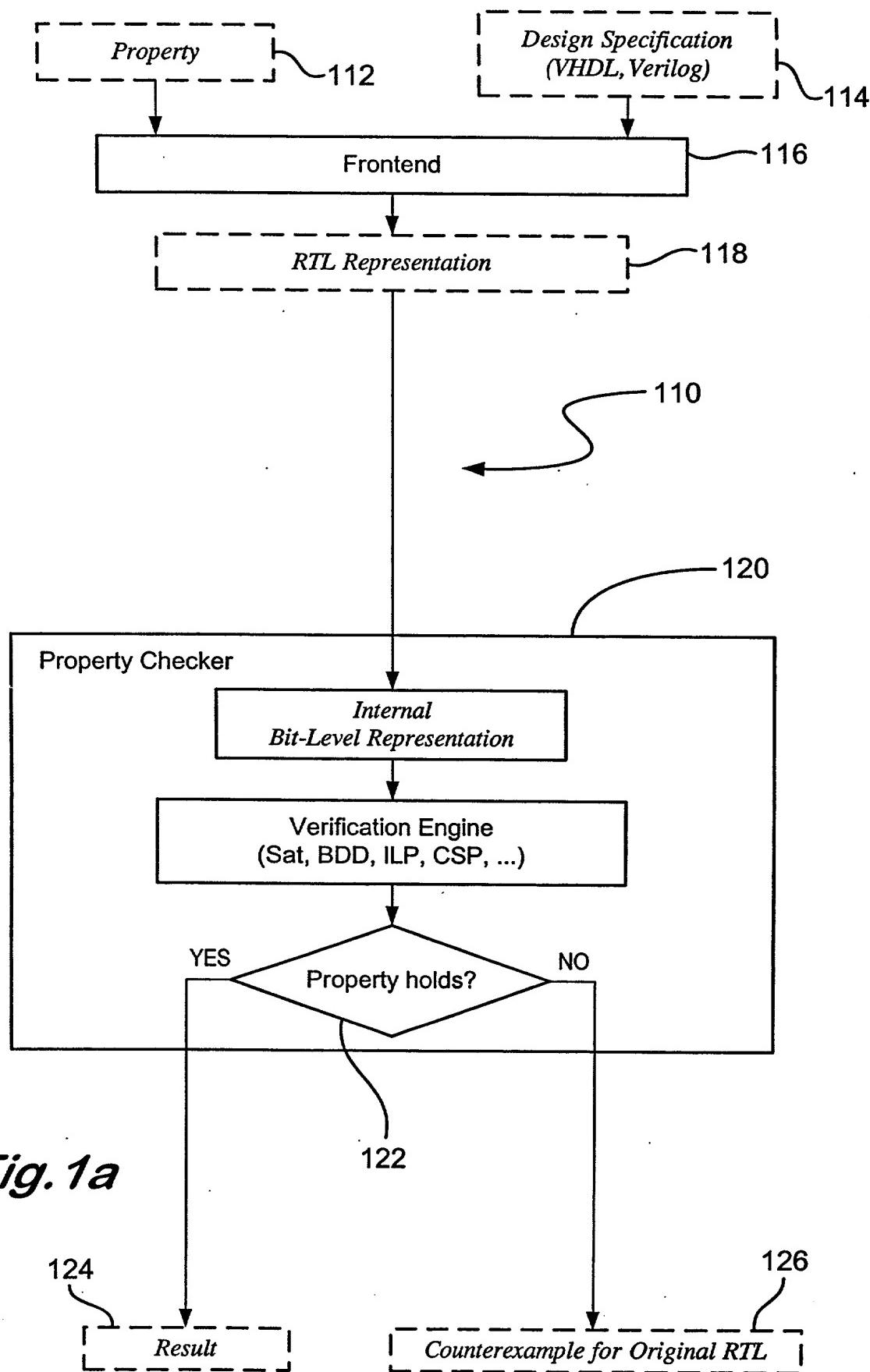
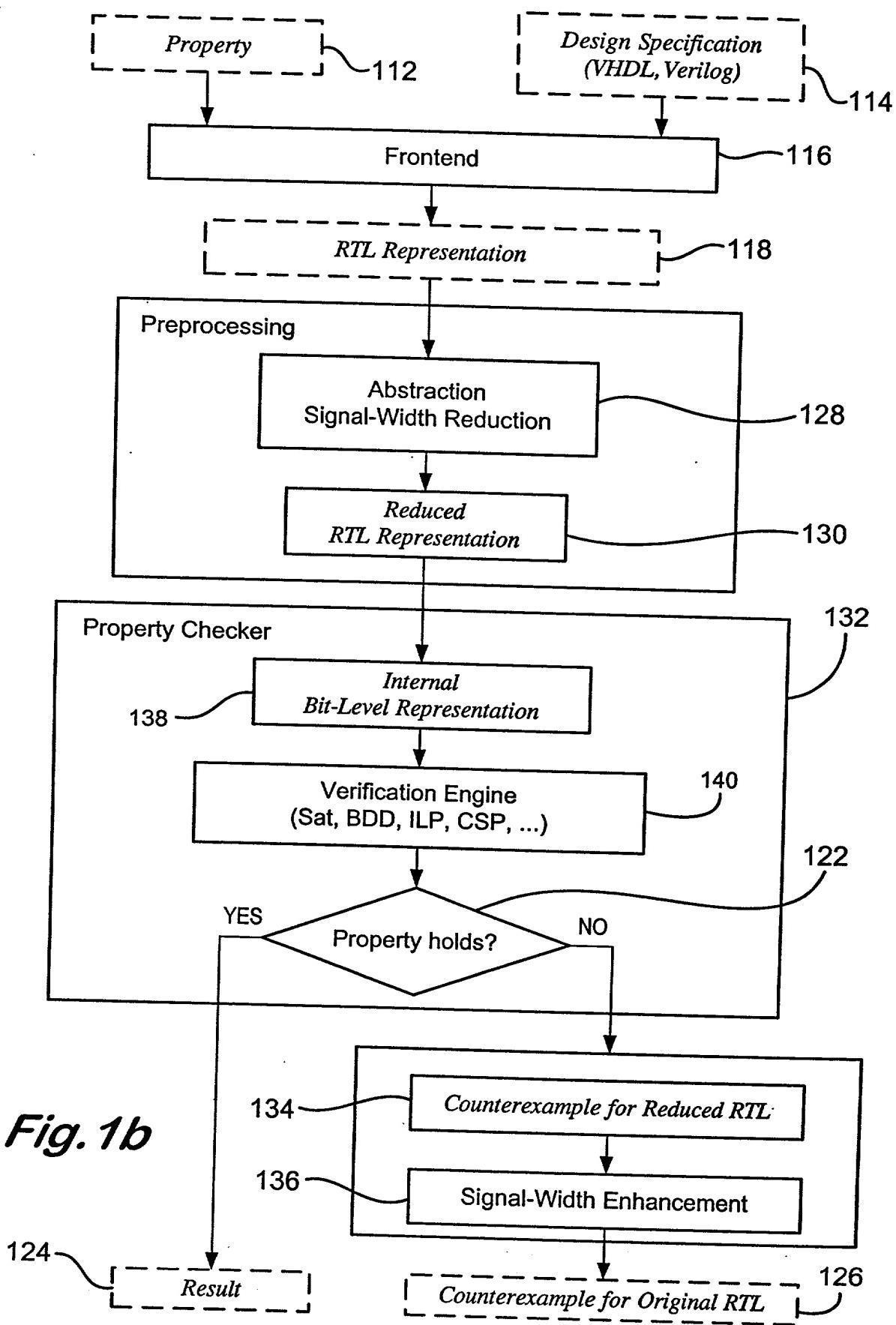


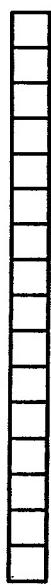
1/19



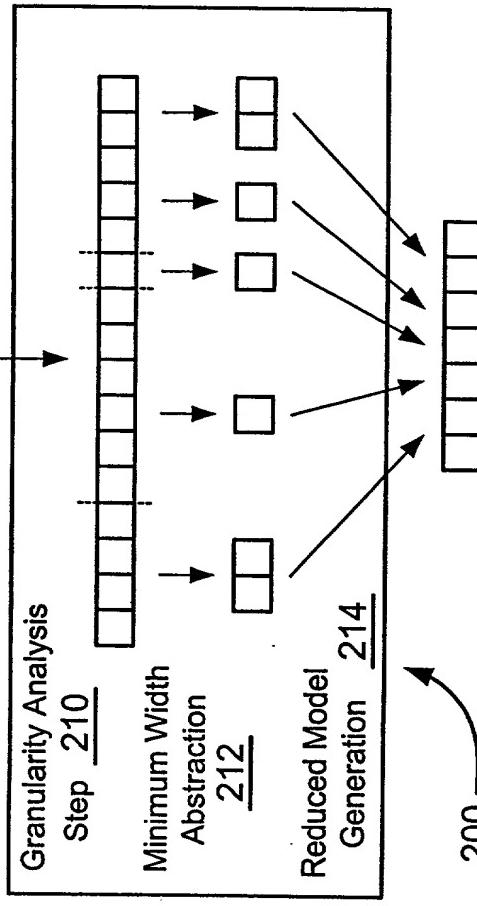
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*Fig. 2*

*Original Signal  $x_{[16]}$   
216*



Chunks :  $x_{[16]}[15,12], x_{[16]}[11,6], x_{[16]}[5,5], x_{[16]}[4,2], x_{[16]}[1,0]$

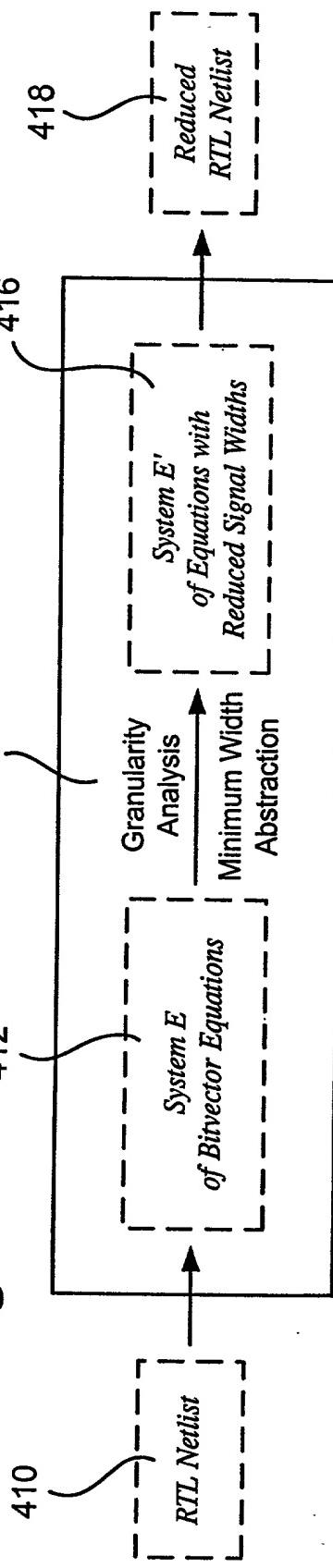
218

Minimum Widths:  $2 + 1 + 1 + 1 + 2 = 7$

220

Corresponding Reduced Signal  $x'[7]$

222

*Fig. 4*

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*Fig. 3*

| Bitvector Operator           | Syntax                              | Example   |
|------------------------------|-------------------------------------|---|
| bitvector variables          | $x_{[n]}$                           | $x_{[8]}, y_{[1]}, z_{[4]}, \dots$  |
| bitvector constants          | $c_{[m]}$                           | $10011_{[5]}, 0011111_{[8]}, 0_{[1]}, 1_{[1]}, \dots$   |
| concatenation                | $\otimes$                           | $x_{[16]} \otimes y_{[4]}$  |
| extraction                   | $[j, i]$                            | $x_{[8]}[5, 2]$   |
| bitwise negation (inversion) |                                     | $\text{neg}(x_{[8]})$   |
| bitwise Boolean operations   | and, or, xor<br>nand, nor, xnor     | $x_{[12]} \text{ and } y_{[12]}, x_{[12]} \text{ or } y_{[12]}, x_{[12]} \text{ xor } y_{[12]}$<br>$x_{[12]} \text{ nand } y_{[12]}, x_{[12]} \text{ nor } y_{[12]}, x_{[12]} \text{ xnor } y_{[12]}$ |
| if-then-else                 | ite                                 | $\text{ite}(a_{[4]} = b_{[4]}, x_{[8]}, y_{[8]})$<br>$\text{ite}(a_{[4]} < b_{[4]}, x_{[8]}, y_{[8]})$  |
| arithmetic                   | $+, -, *$                           | $x_{[32]} + y_{[32]}, x_{[32]} - y_{[32]}$<br>$x_{[16]} * y_{[16]}$   |
| memory read                  | $\text{mem}_{[m \cdot n]}[i_{[n]}]$ | $x_{[10]} := \text{mem}_{[128 \cdot 10]}[i_{[7]}]$  |
| memory write                 |                                     | $\text{mem}_{[32 \cdot 8]}[i_{[5]}] := x_{[8]}$   |

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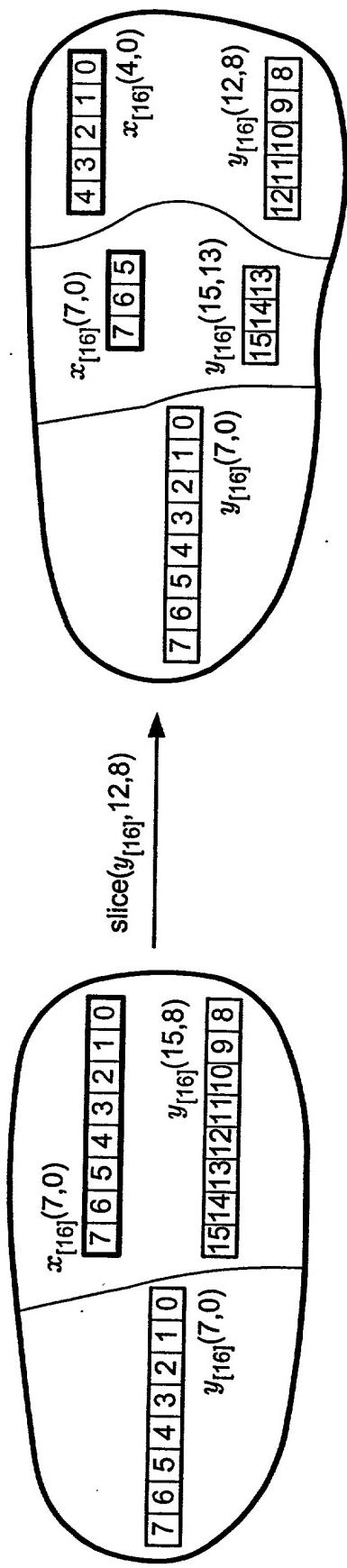


Fig. 5

### Bitvectors

$$\begin{array}{ll}
 a_{[2]} & \boxed{1} \boxed{0} \\
 x_{[8]} & \boxed{7} \boxed{6} \boxed{5} \boxed{4} \boxed{3} \boxed{2} \boxed{1} \boxed{0} \\
 z_{[4]} & \boxed{3} \boxed{2} \boxed{1} \boxed{0}
 \end{array}$$

Dependency Classes of Bitvector Chunks

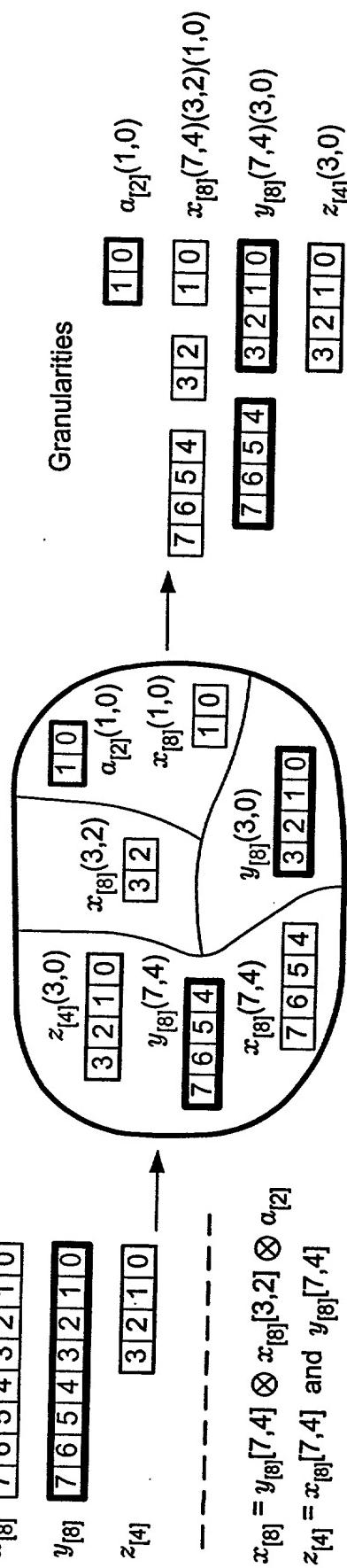


Fig. 6

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**Fig. 6a****Process 1 Granularity Analysis of Bitvector Equations**

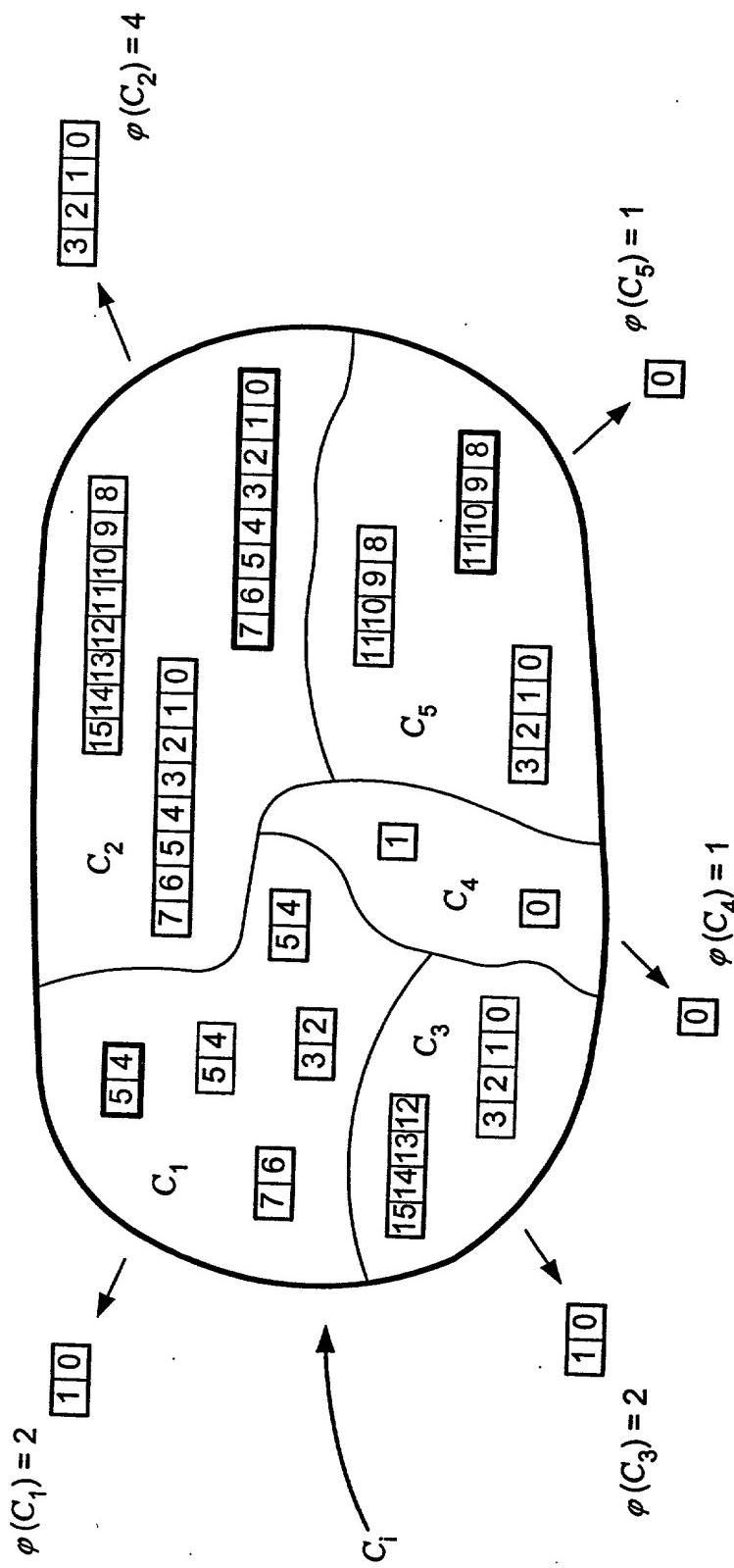
```

1  gran(e) {
2    switch (e) {
3      case e ≡ 's[n] = t1[m1] ⊕ t2[m2]' :
4        case e ≡ 's[n] = (t1[m1] ⊕ t2[m2]) [j, i]' :
5          gran('s[n][n-1, m2] = t1[m1]'); gran('s[n] [m2-1,0] = t2[m2]');
6            if (j < m2) {
7              gran('s[n] = t2[m2] [j, i]');
8            } else if (i ≥ m2) {
9              gran('s[n] = t1[m1] [j-m2, i-m2]');
10            } else {
11              gran('s[n] = t1[m1] [j-m2, 0] ⊕ t2[m2] [m2-1, i]');
12            }
13          }
14        }
15      case e ≡ 's[n] = (t1[m] [l, k]) [j, i]' :
16        case e ≡ 's[n] = ite(a[m] = b[m], t1[n], te[n]) [j, i]' :
17          case e ≡ 's[n] = (t1[m] and t2[m]) [j, i]' :
18          ...
19        case e ≡ 'x[n] [j, i] = y[m] [l, k]' :
20      }

```

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Fig. 7



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*Fig. 7a*

---

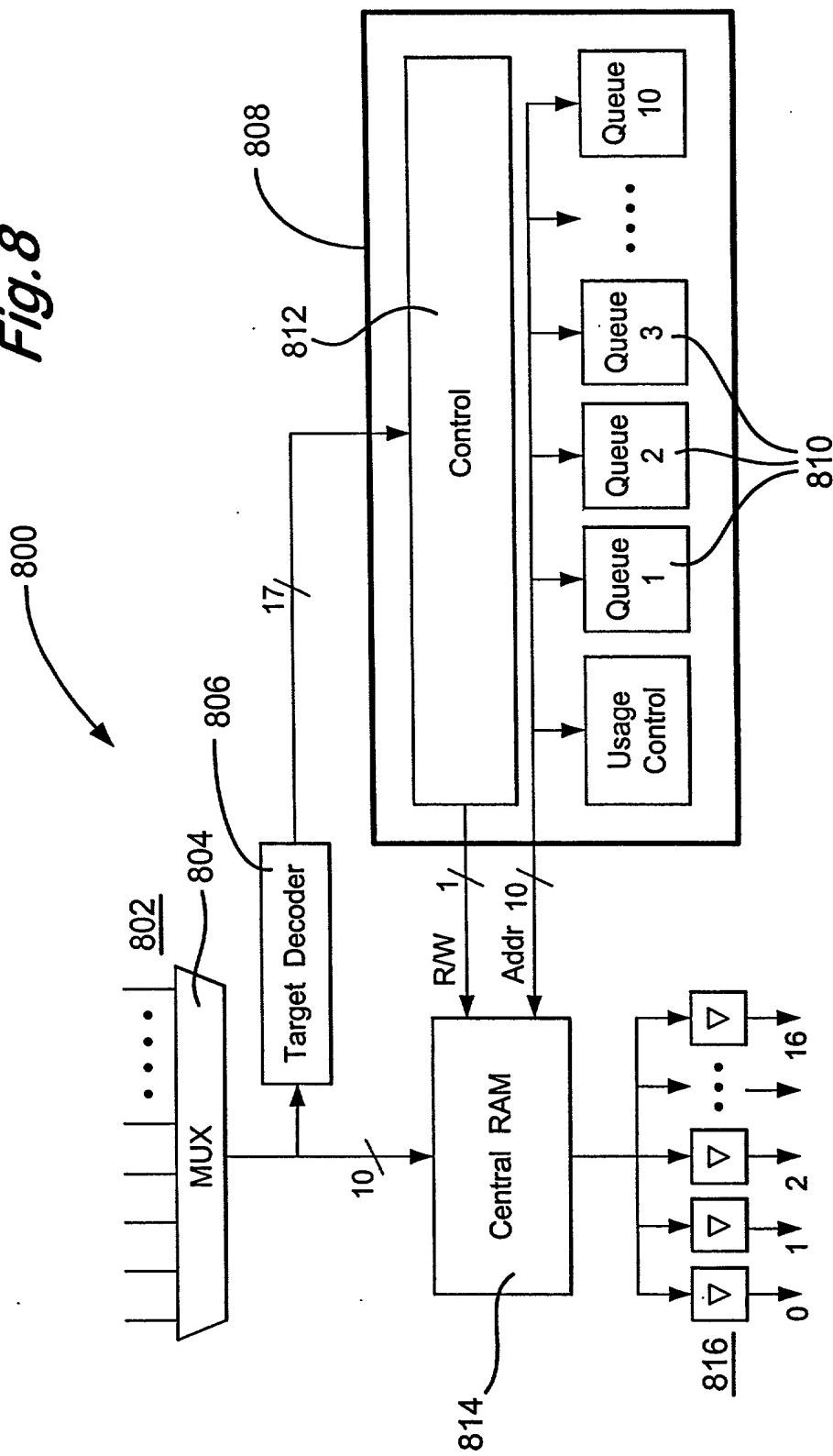
Process 2 Reduced Model Generation

---

```
1 for each bitvector variable  $x_{[n]}$  {  
2    $m := 0$  ;  
3   for each chunk  $x_{[n]} \langle j, i \rangle$  of the computed granularity of  $x_{[n]}$  {  
4      $C := find(x_{[n]} \langle j, i \rangle)$  ; // equivalence class containing  $x_{[n]} \langle j, i \rangle$   
5      $m := m + \varphi(C)$  ;  
6   }  
7   if ( $m \geq n$ ) then  $m := n$  ;  
8   replace all occurrences of  $x_{[n]}$  in the bivector equations by  $x'_{[m]}$  ;  
9   and adjust all extraction expressions affected by  $x_{[n]}$  ;  
10 }
```

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Fig.8

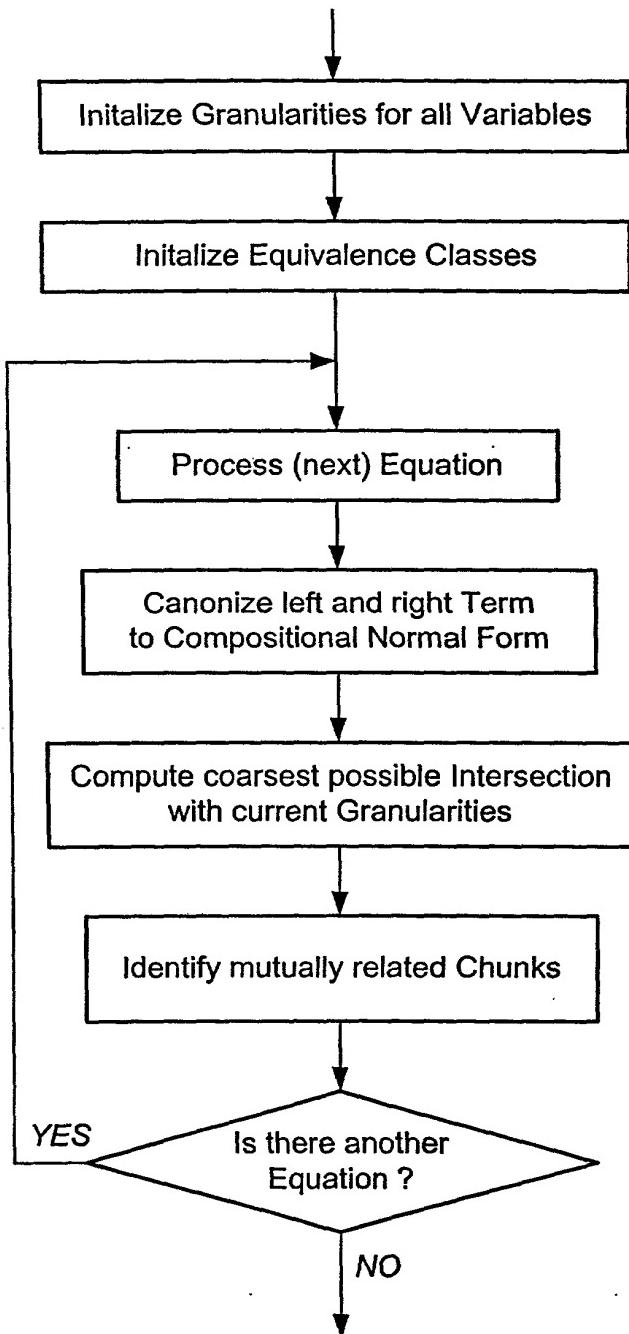


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*Fig. 9*

|   | Property                                | Original design  | Reduced model  |
|---|---|--|--|
| Computation times of the prototype for analysis and reduced model generation        | nop<br>read<br>write                    |  | 2.96 secs<br>6.53 secs<br>3.24 secs  |
| FIFO sizes on RTL   | nop<br>read<br>write                    | 160 cells x 10 bit<br>160 cells x 10 bit<br>160 cells x 10 bit | 160 cells x 2 bit<br>160 cells x 3 bit<br>160 cells x 3 bit                          |
| Overall number of bits in all relevant signals (cones of influence of the property) | nop<br>read<br>write                    | 20925<br>31452<br>14622  | 5034 (24.0 %)<br>10592 (33.6 %)<br>5163 (35.3 %)                                     |
| Overall number of gates in synthesized netlist                                      | nop<br>read<br>write                    | 23801<br>23801<br>23801  | 5661 (27.9 %)<br>7929 (33.3 %)<br>7929 (33.3 %)                                      |
| Number of state bits  | nop<br>read<br>write                    | 1658<br>1658<br>1658   | 362 (21.8 %)<br>524 (31.6 %)<br>524 (31.6 %)   |
| Property checker runtimes   | nop<br>read<br>write_fail<br>write_hold |  | 37.96 secs ( 2.7 %)<br>3:27 min ( 8.1 %)<br>25.66 secs (19.5 %)<br>1:08 min ( 4.2 %) |

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$$x_{[8]}[7,2] \otimes x_{[8]}[1,0] = \\ (a_{[4]} \otimes b_{[4]} \otimes c_{[4]})[11,4]$$

$$x_{[8]}[7,0] = a_{[4]}[3,0] \otimes b_{[4]}[3,0]$$

$$x_{[8]}[7,4] \otimes x_{[8]}[3,0] = \\ (a_{[4]}[3,0] \otimes b_{[4]}[3,0])$$

$$x_{[8]}[7,4] \longleftrightarrow a_{[4]}[3,0] \\ x_{[8]}[3,0] \longleftrightarrow b_{[4]}[3,0]$$

Fig. 10

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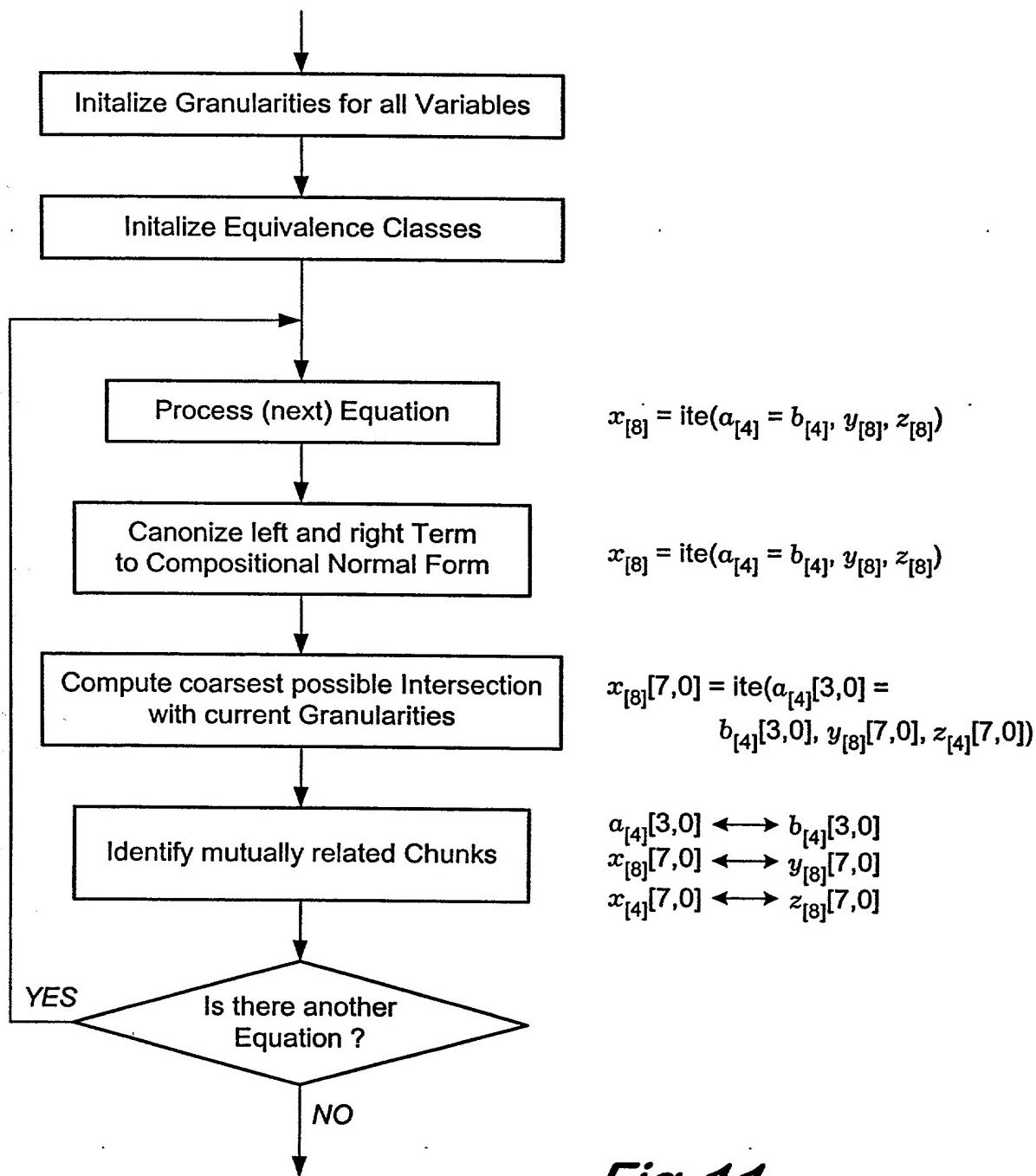


Fig. 11

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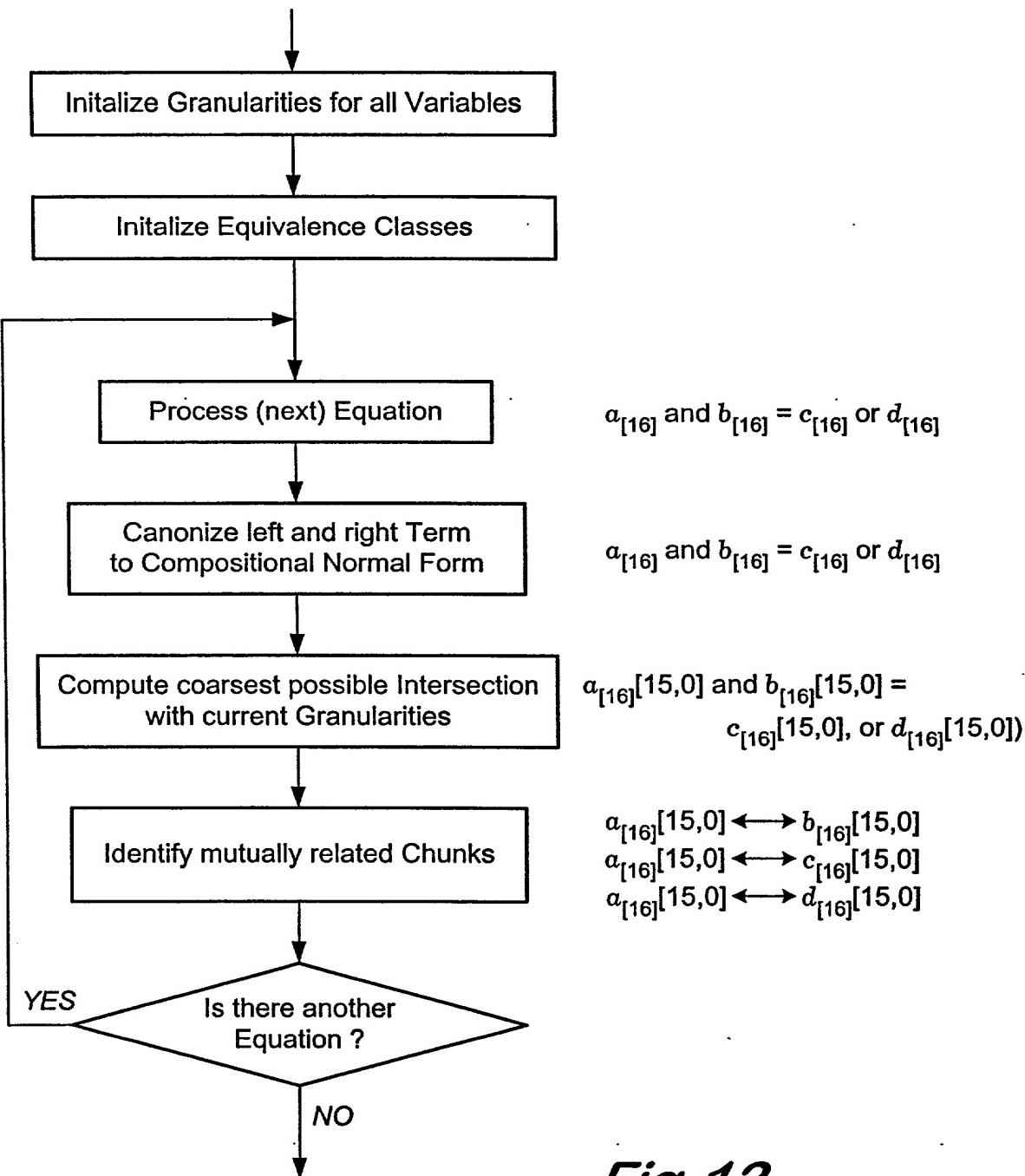


Fig. 12

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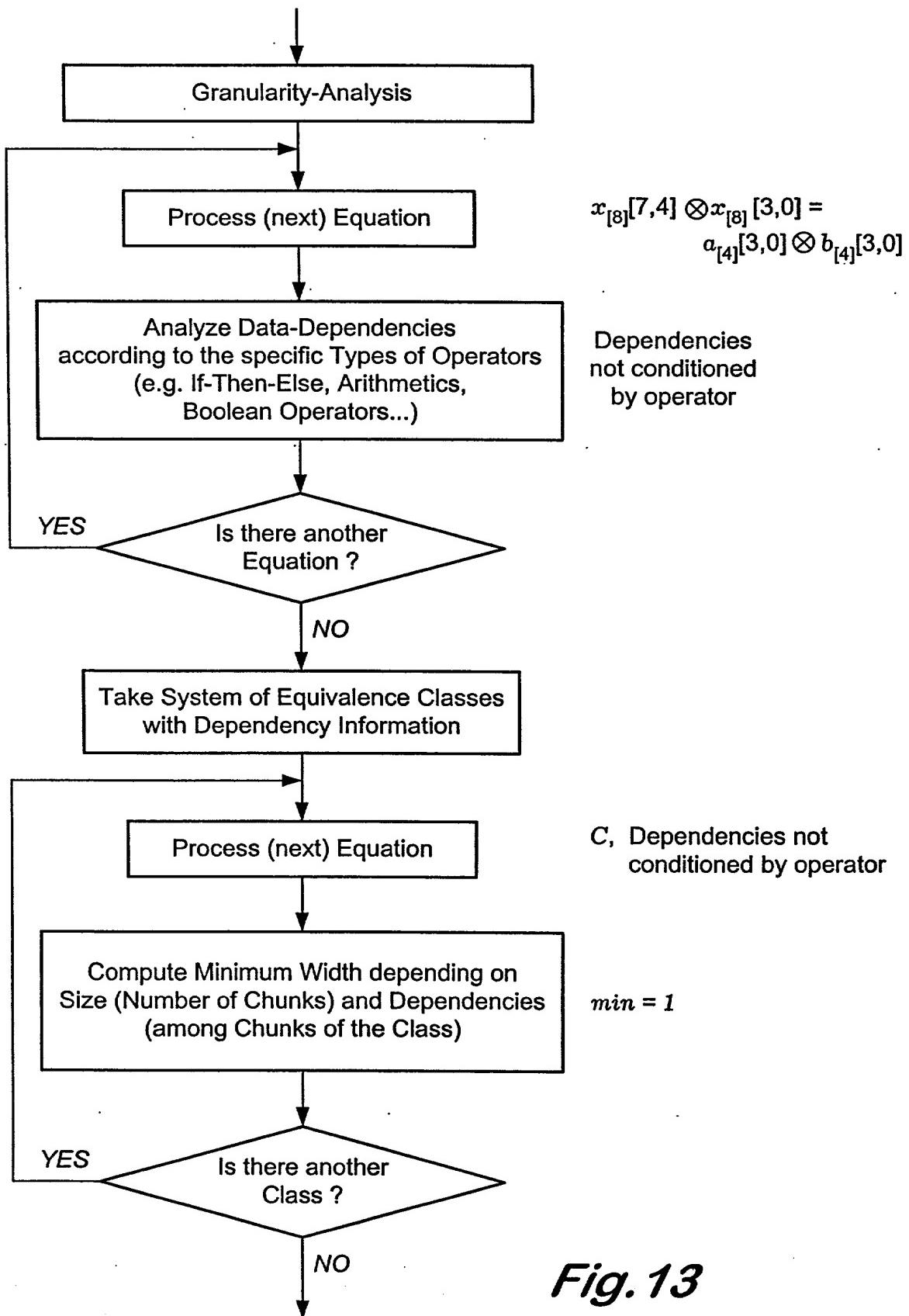
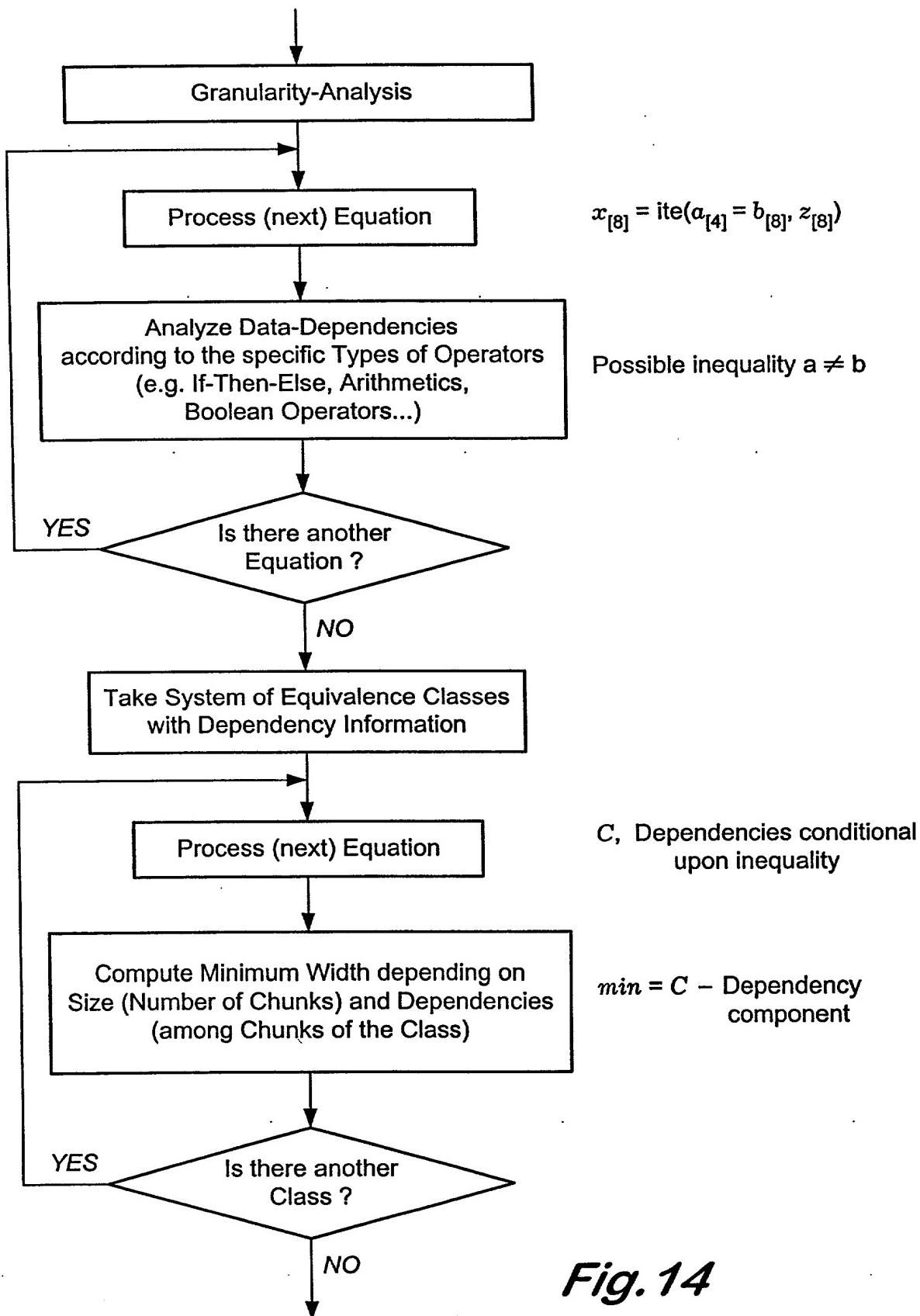


Fig. 13

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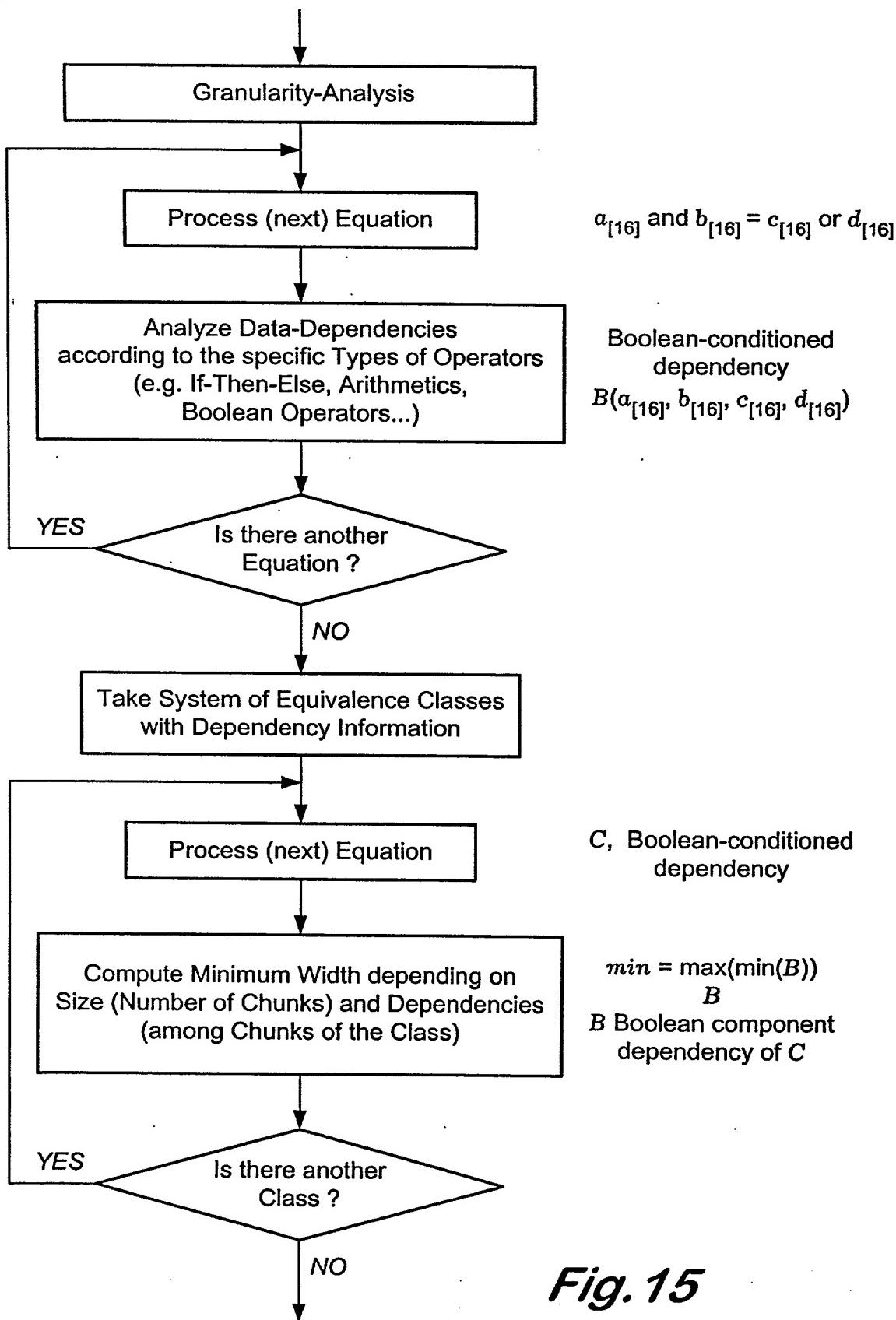


Fig. 15

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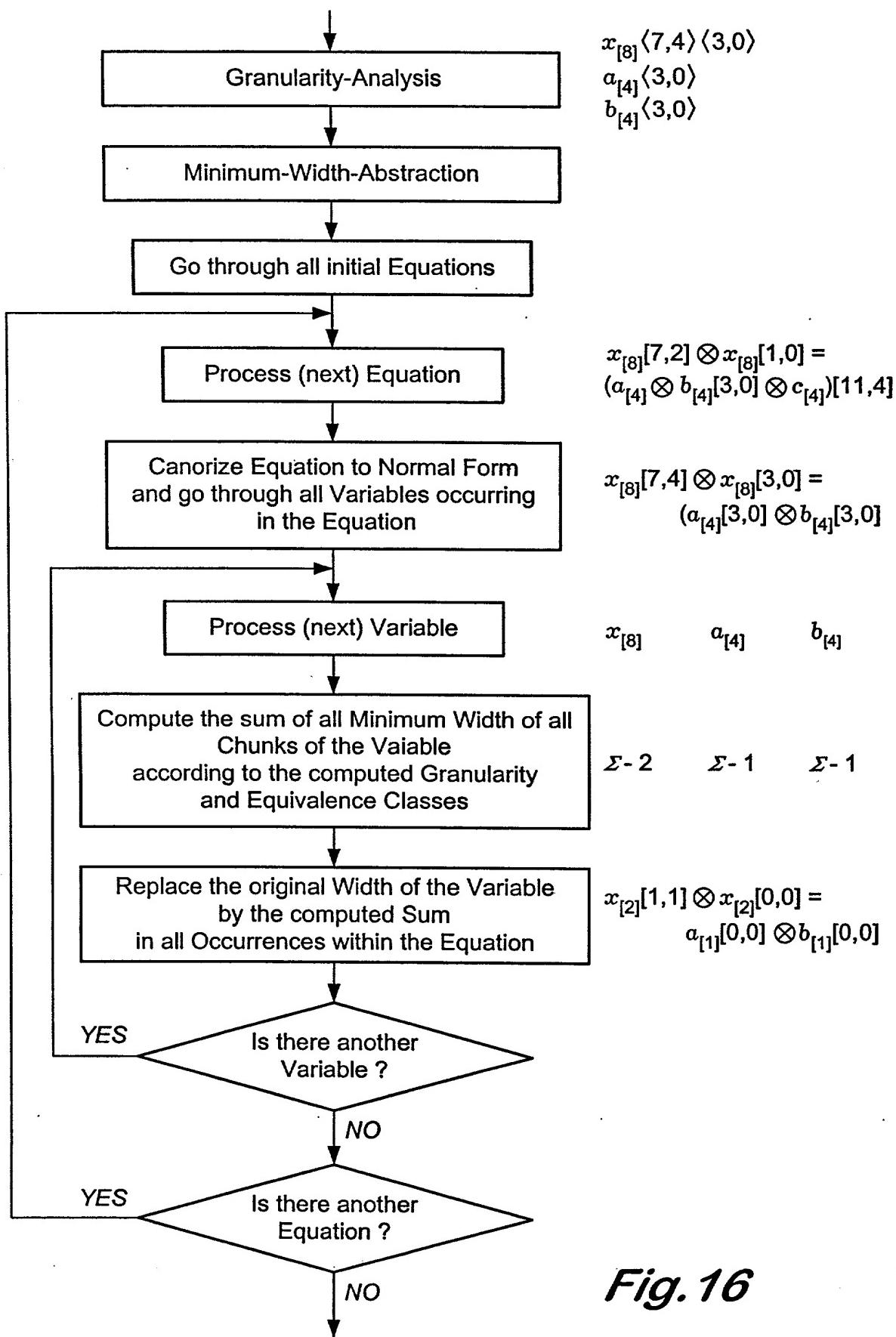


Fig. 16

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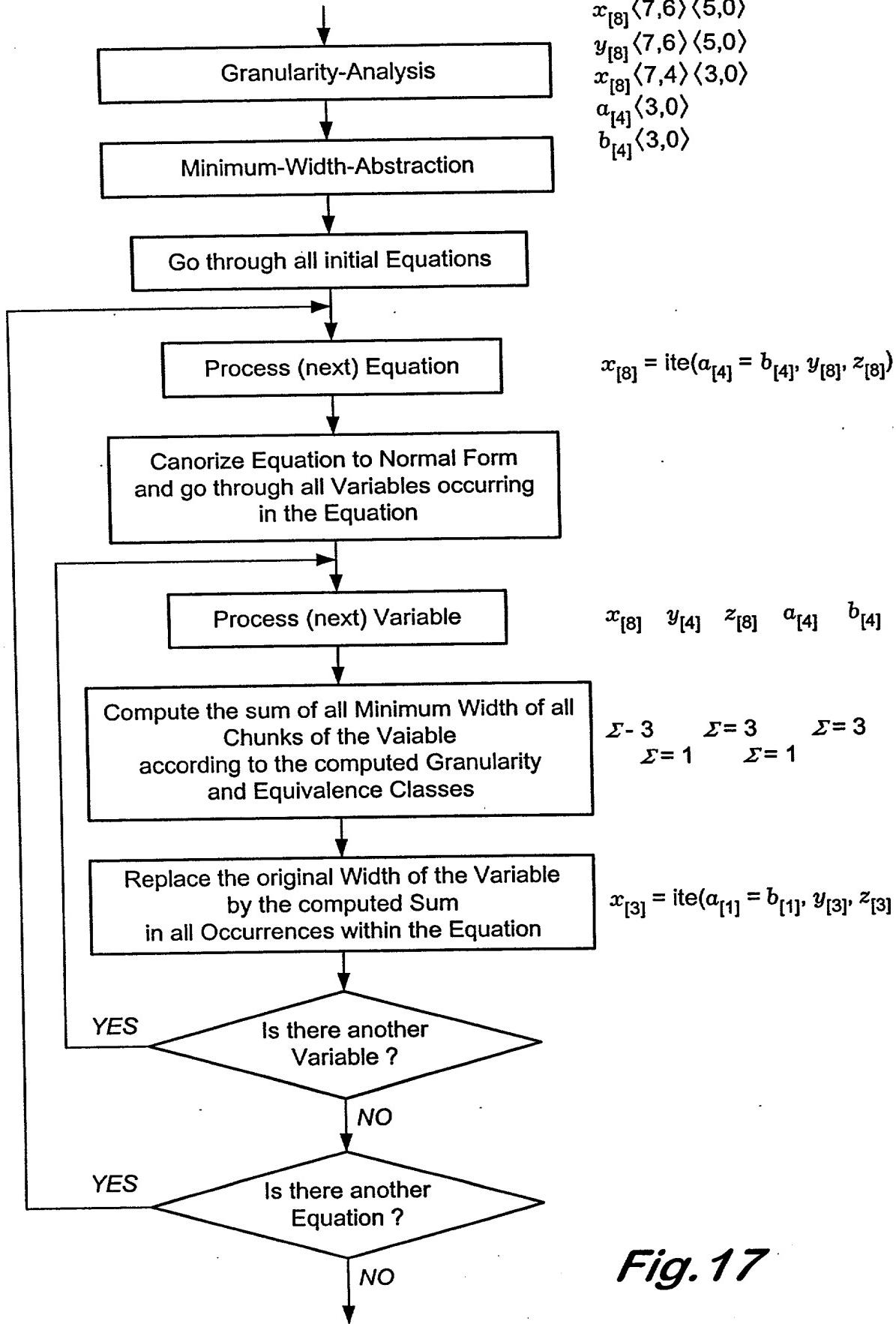


Fig. 17

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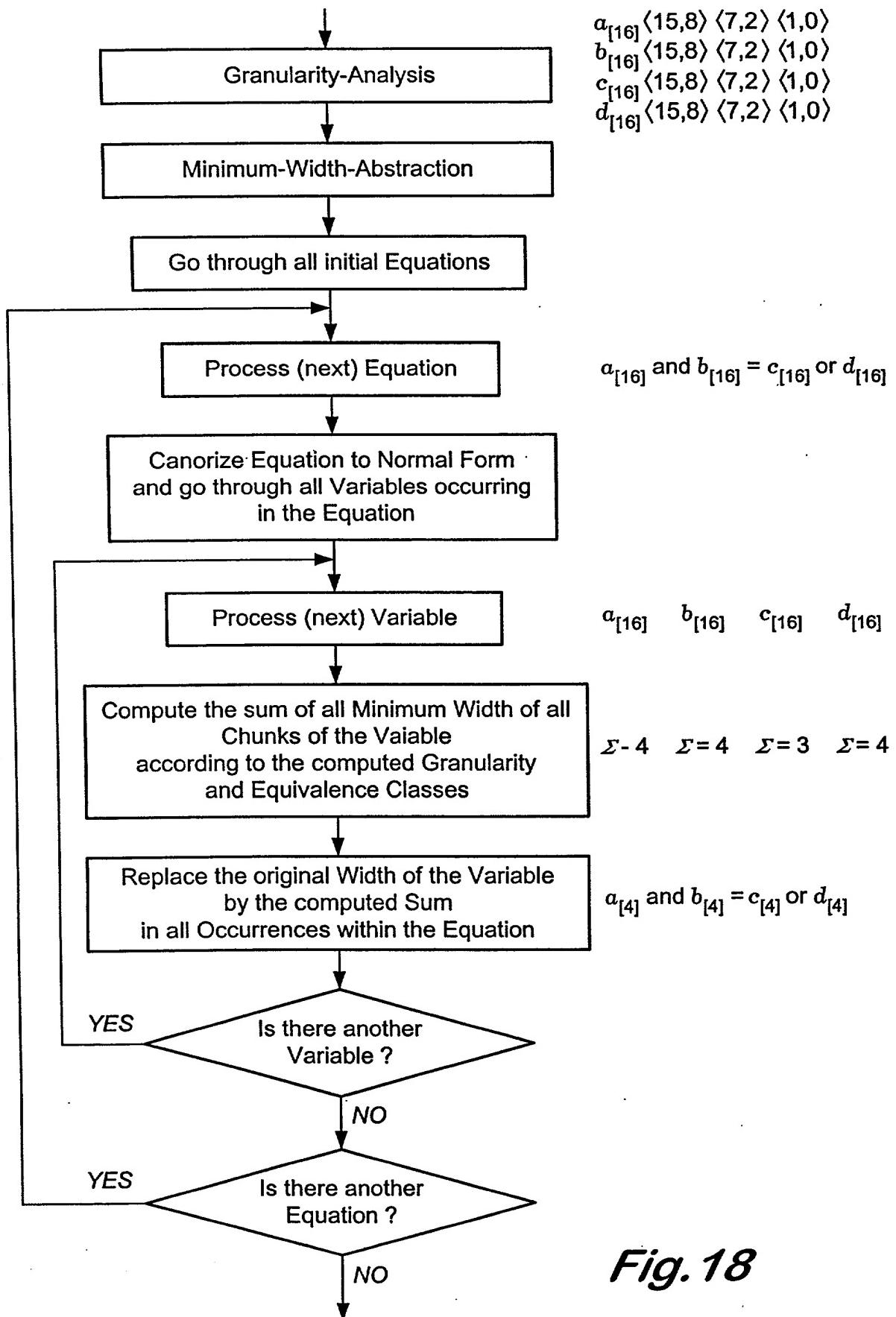


Fig. 18